

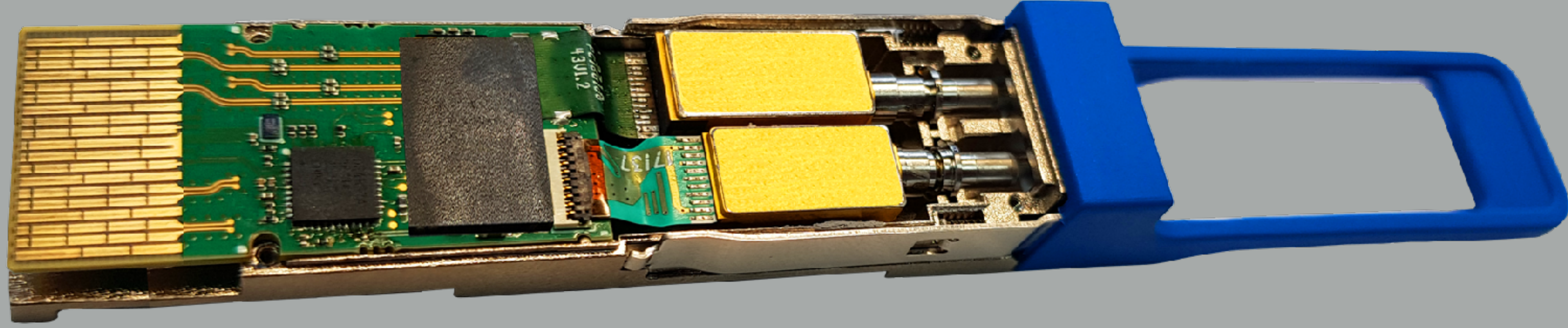
the complexity of
hyper speed
transceivers



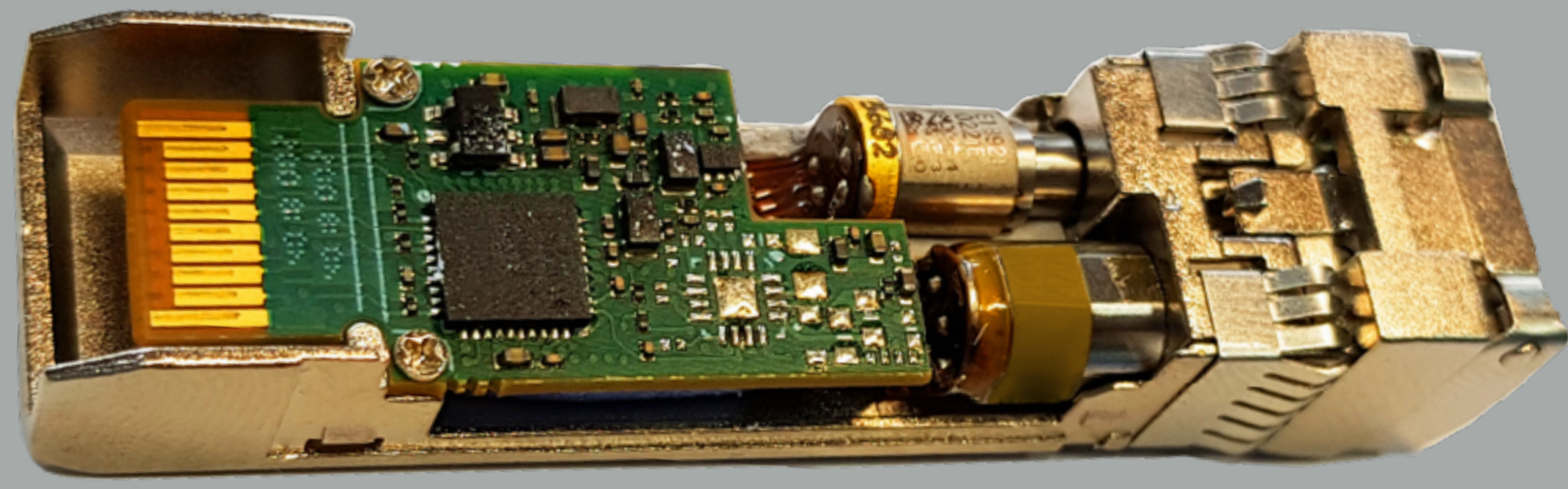
 FLEXPOTIX

lets make it

[400G]

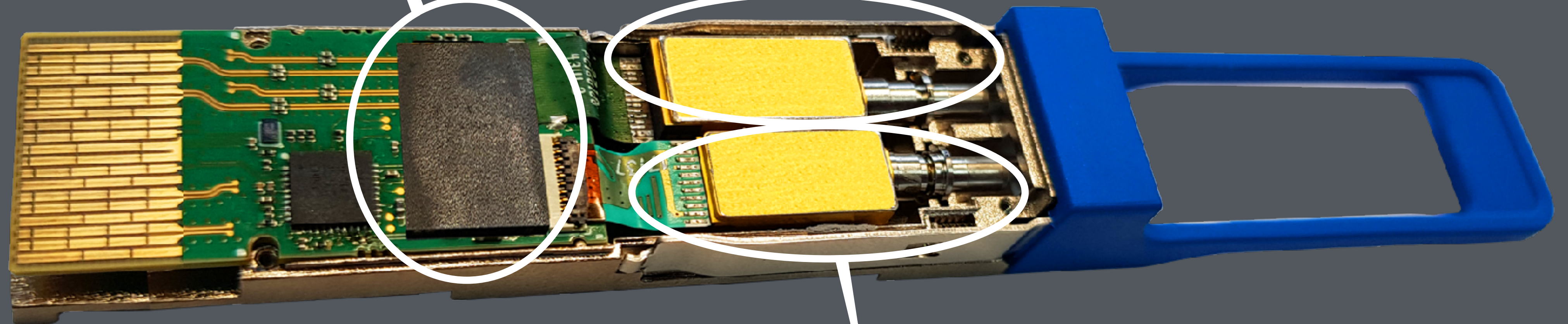


[10G]



DigitalSignalProcessor

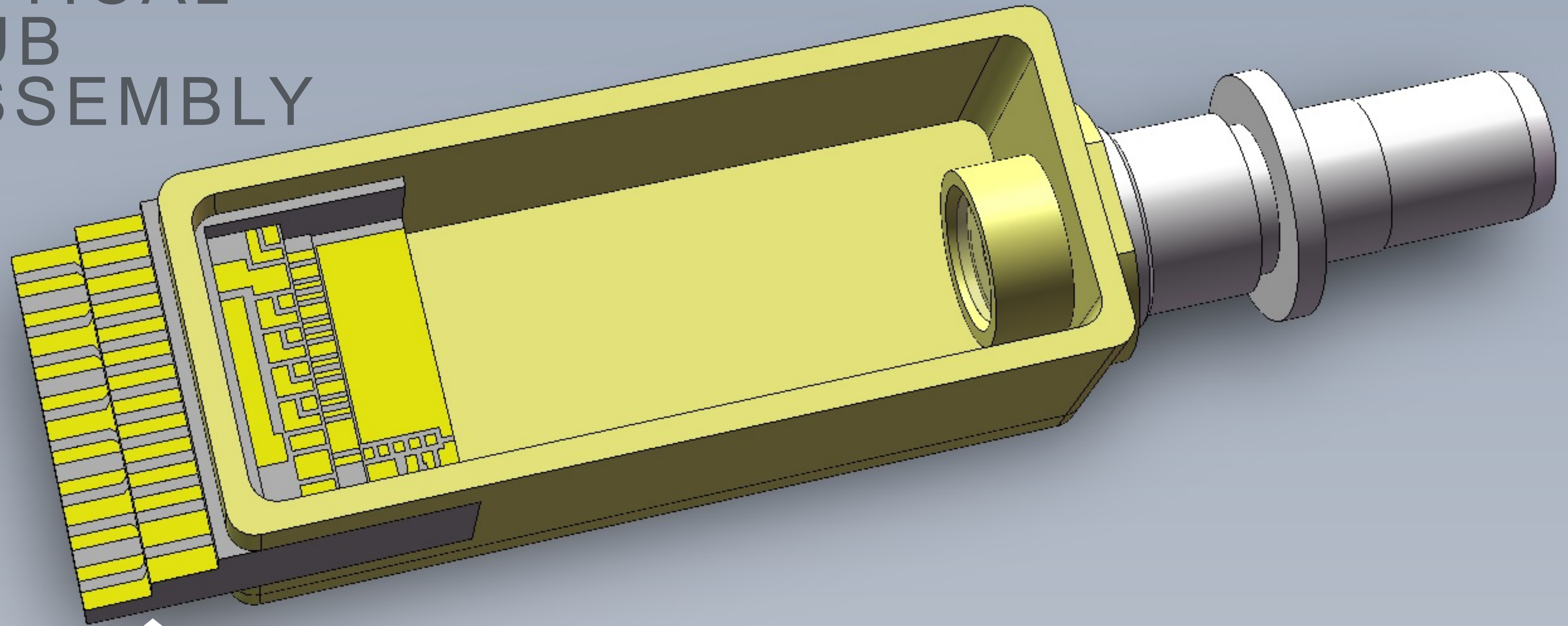
TRANSmitter



reCEIVER

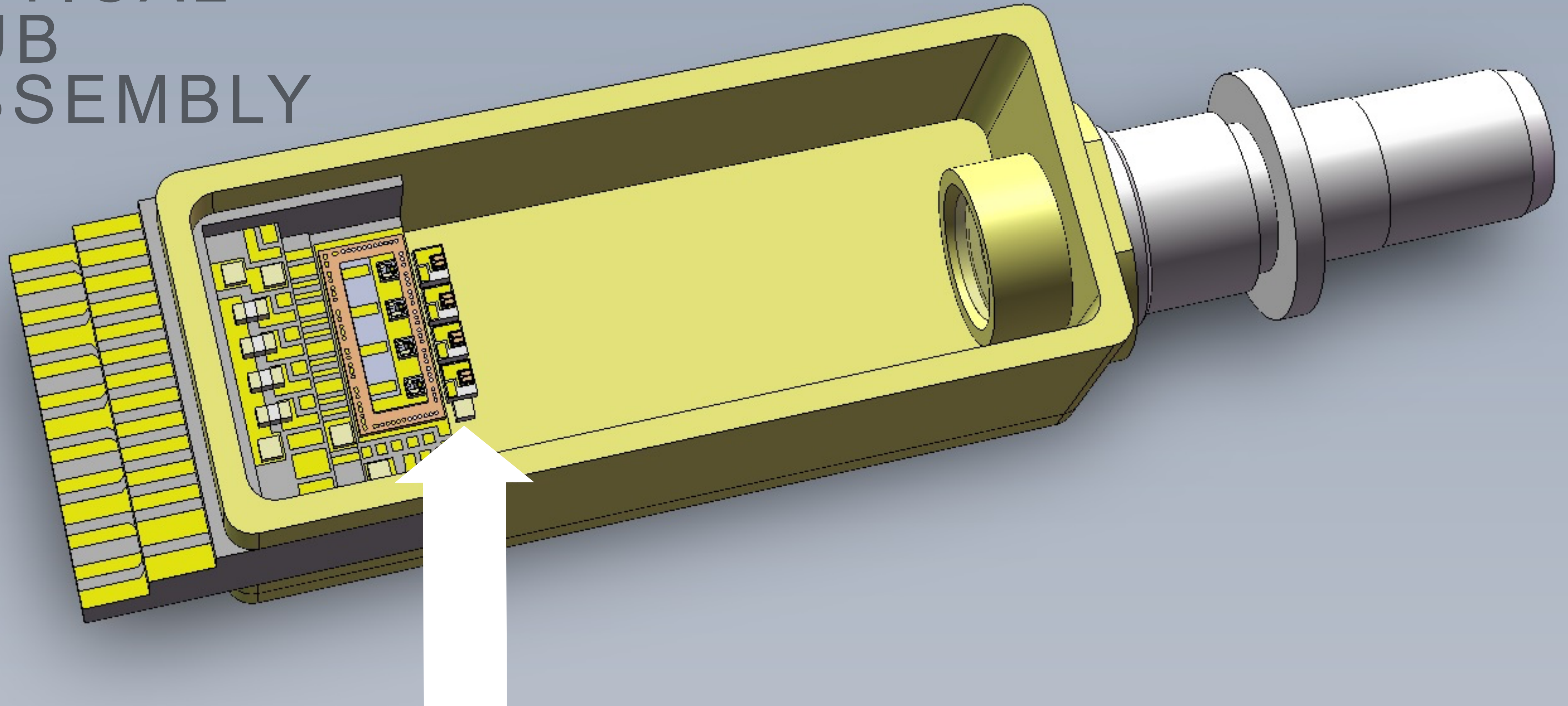
TRANSMITTER

TRANSMITTER
OPTICAL
SUB
ASSEMBLY



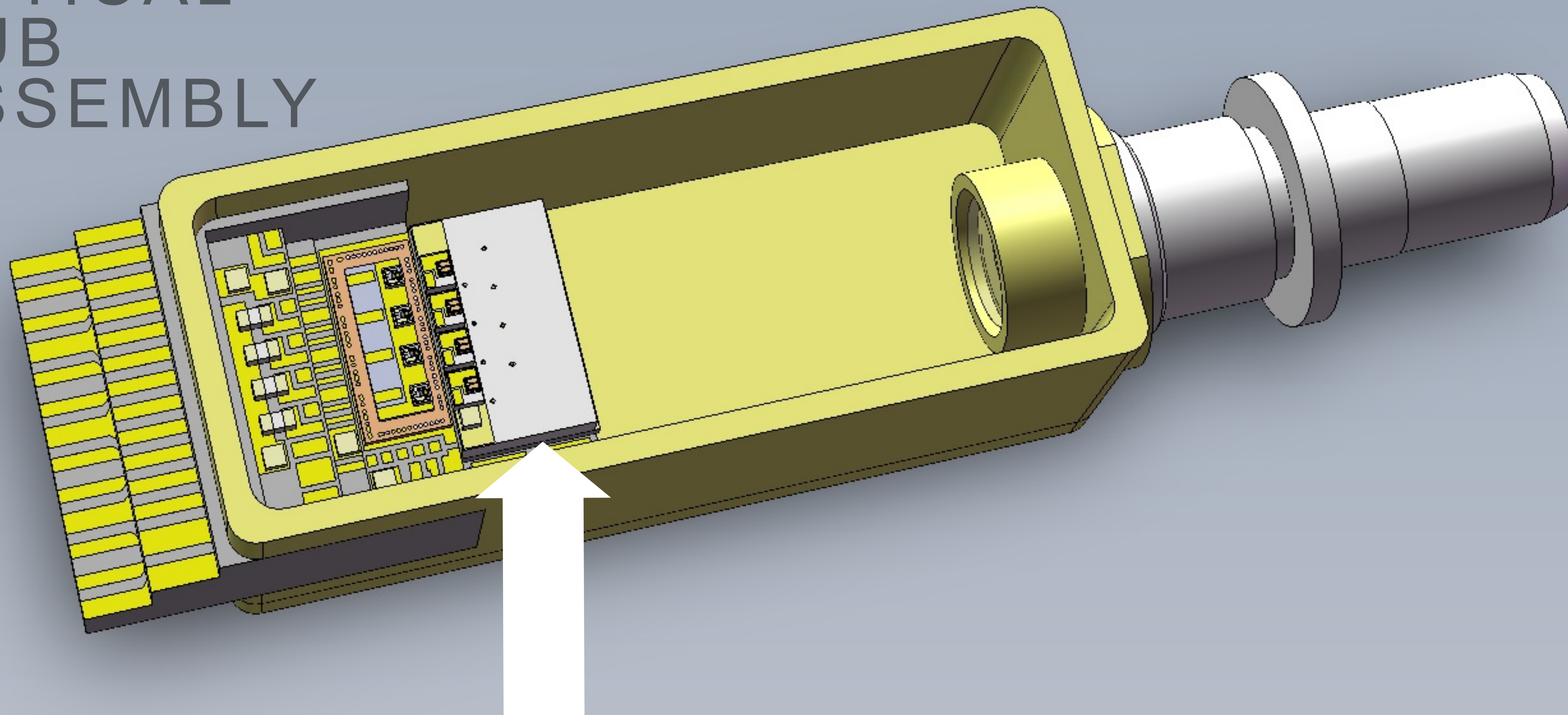
electrical connector

TRANSMITTER
OPTICAL
SUB
ASSEMBLY



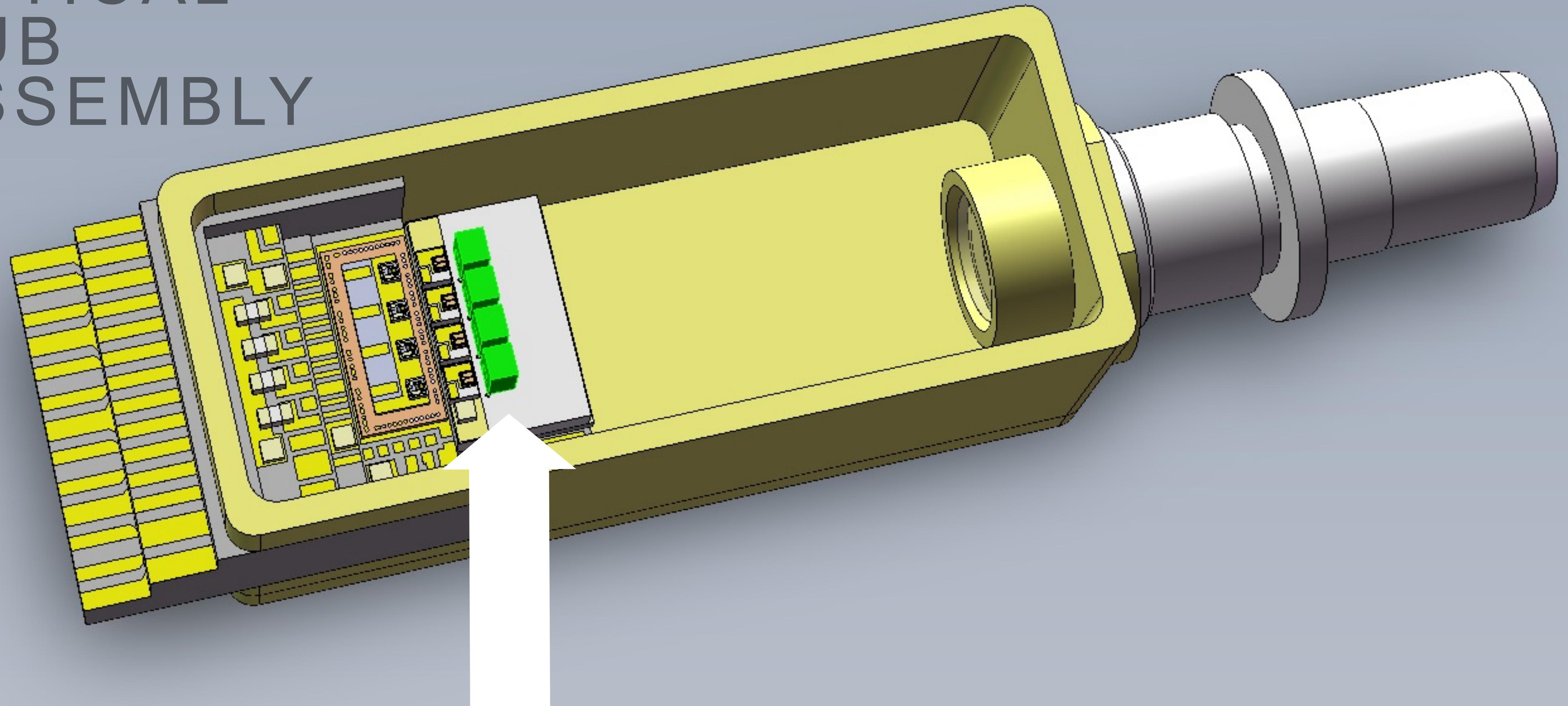
laser diodes

TRANSMITTER OPTICAL SUB ASSEMBLY



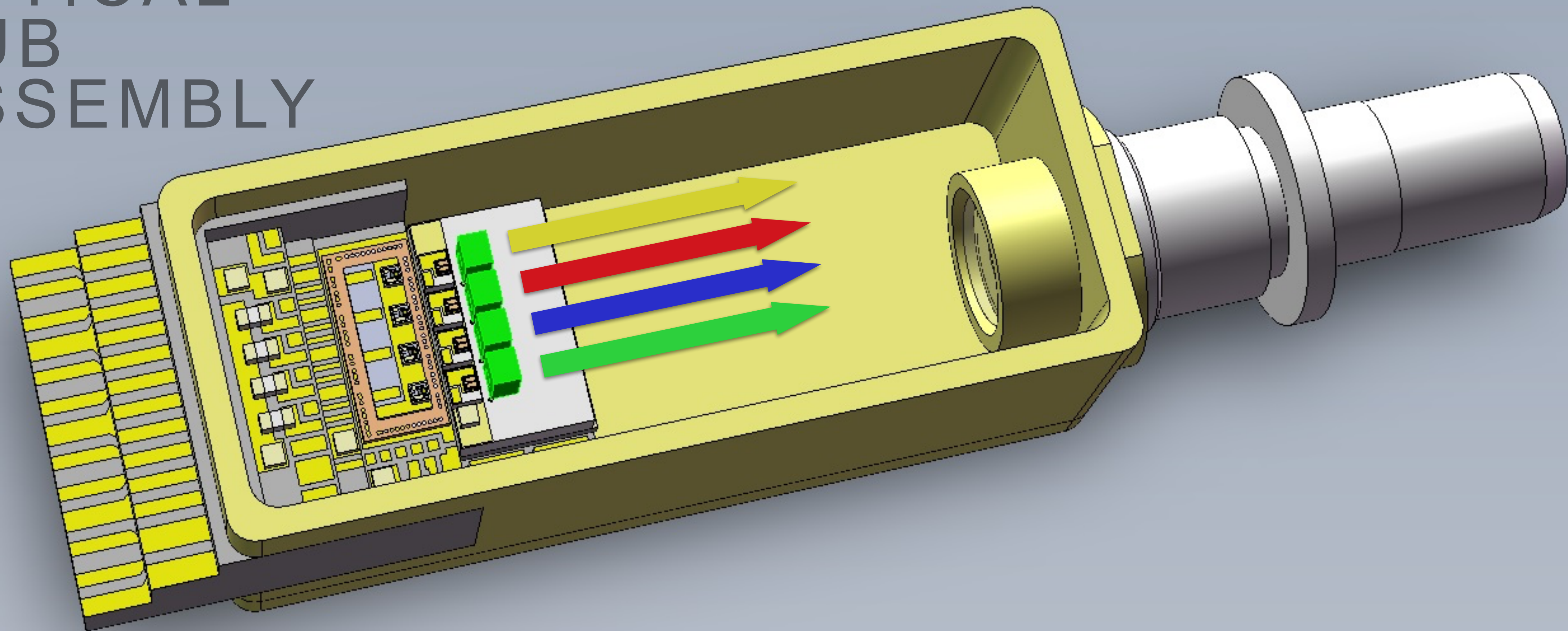
cooler

TRANSMITTER
OPTICAL
SUB
ASSEMBLY

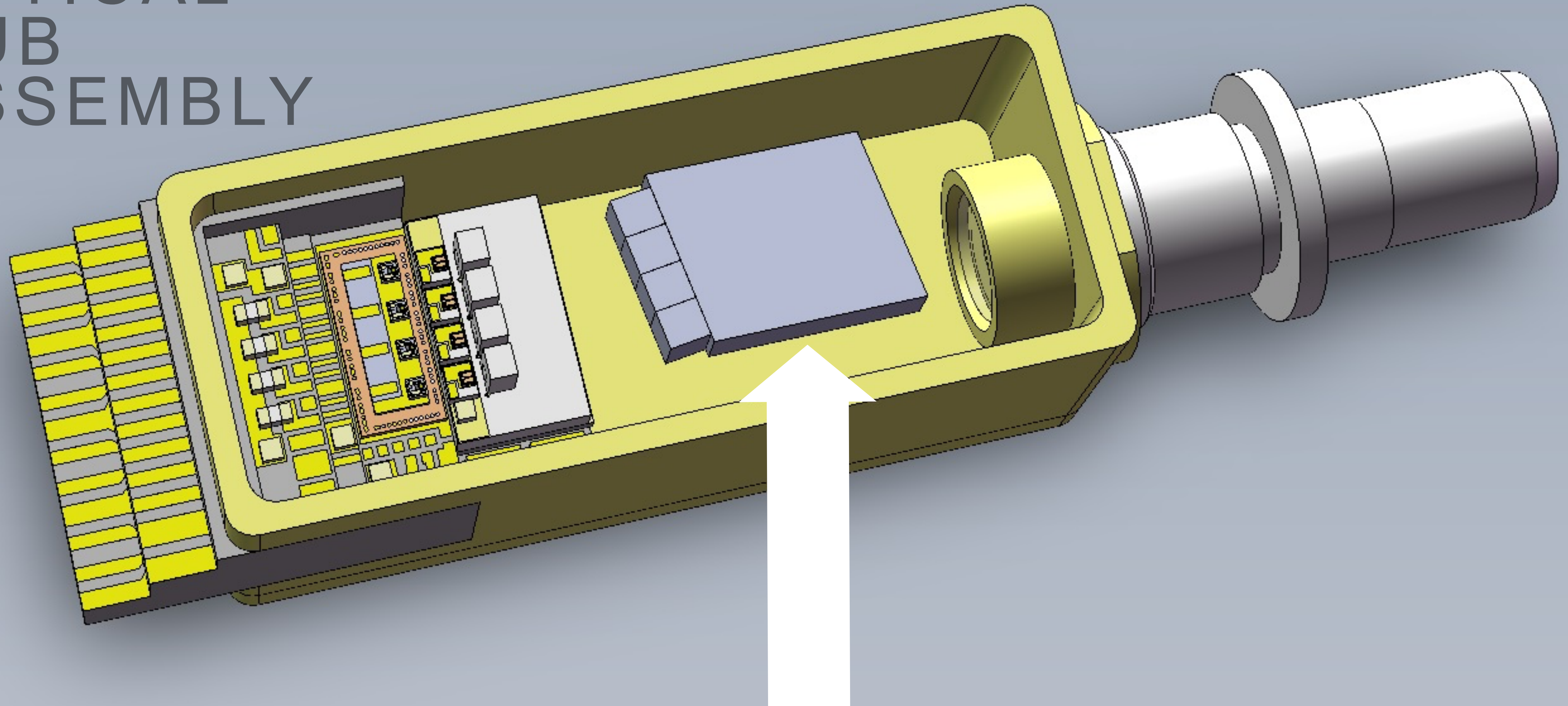


optical lenses

TRANSMITTER OPTICAL SUB ASSEMBLY

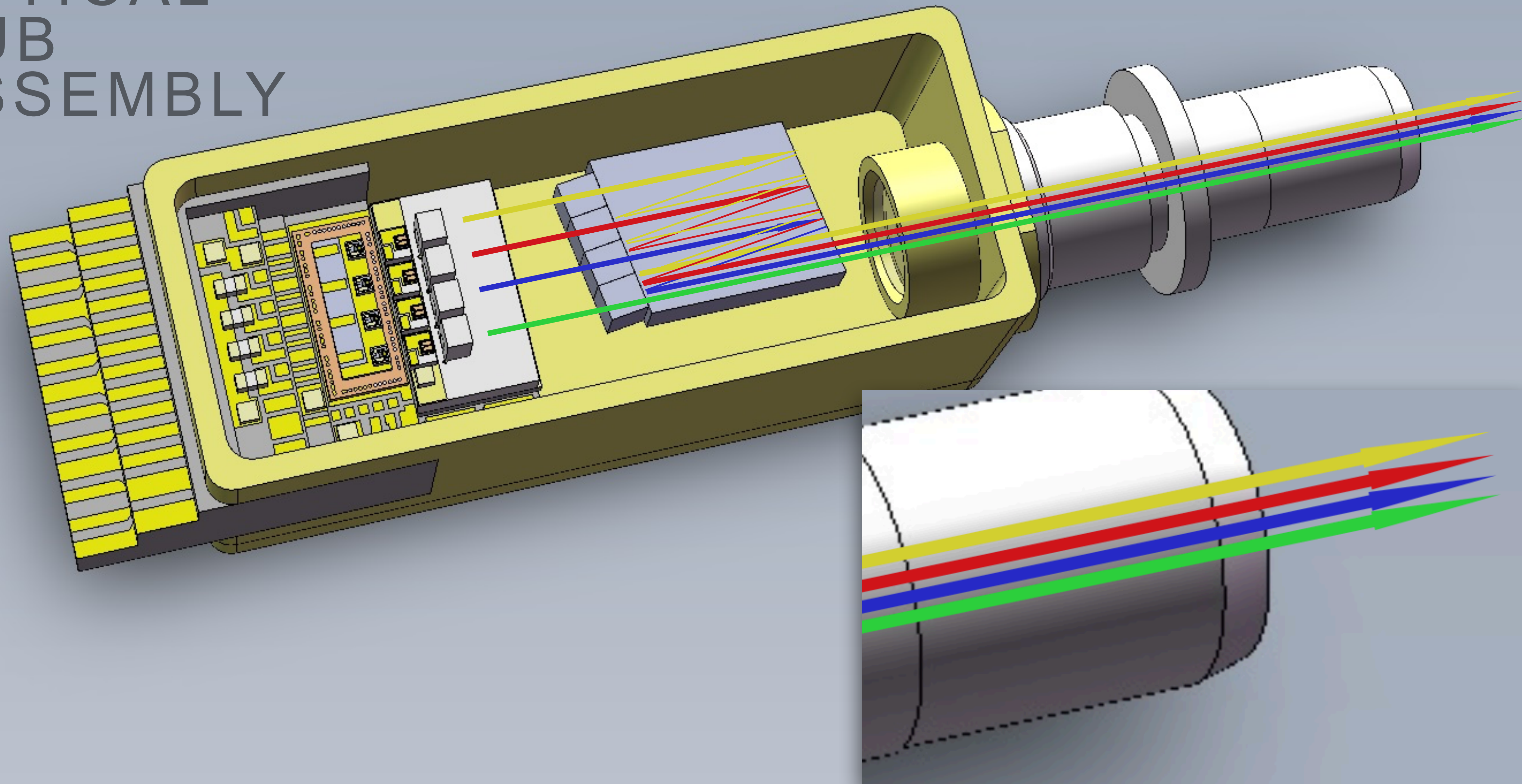


TRANSMITTER
OPTICAL
SUB
ASSEMBLY



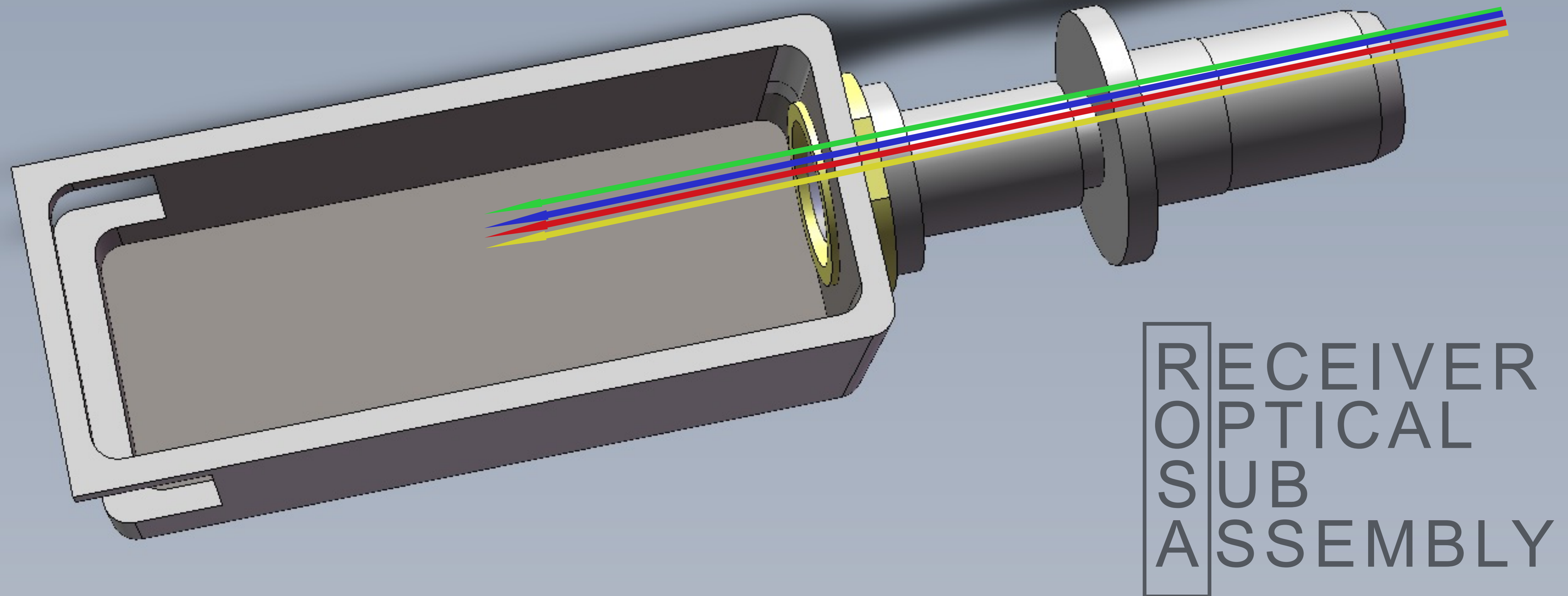
optical multiplexer

TRANSMITTER OPTICAL SUB ASSEMBLY

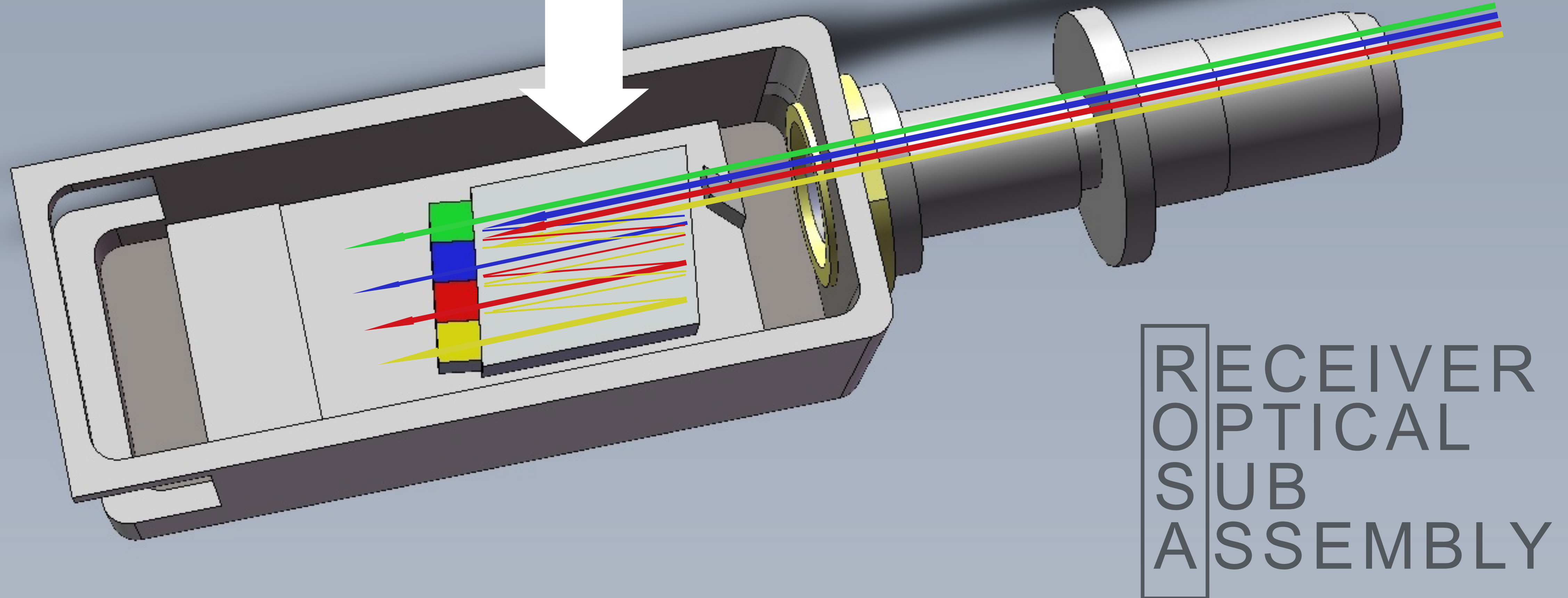


RECEIVER

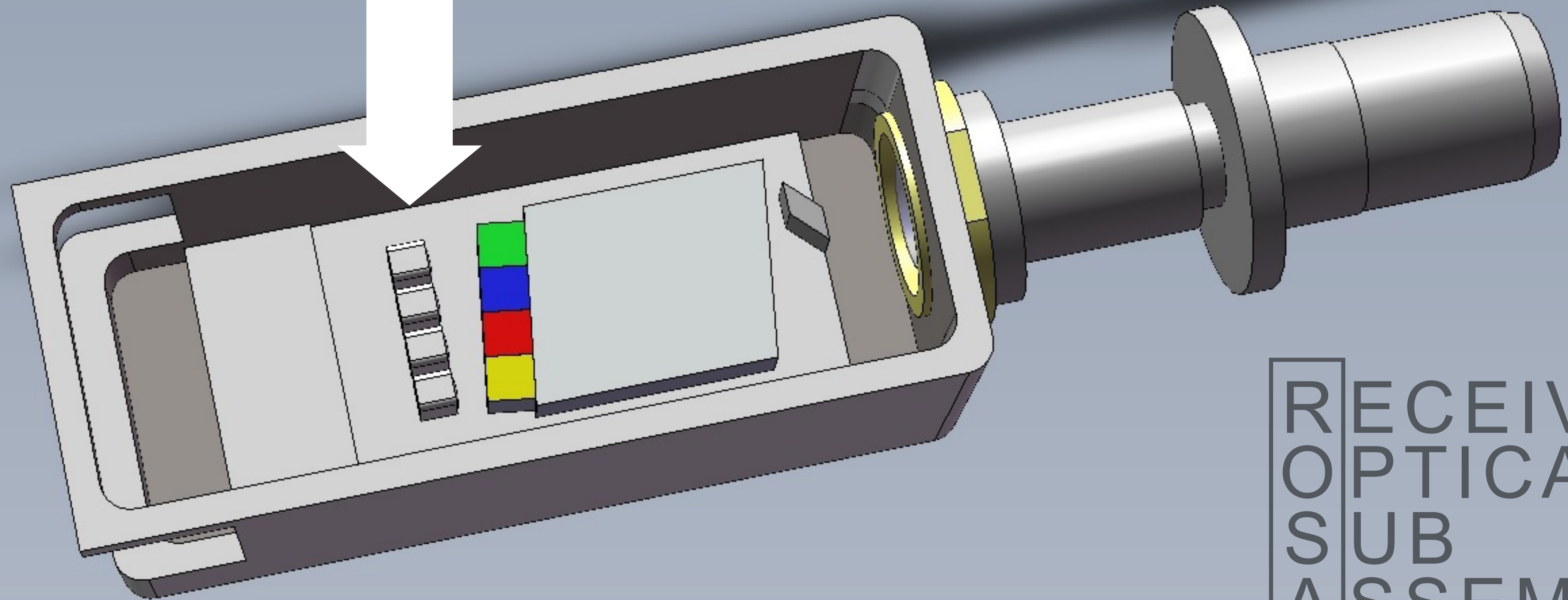
optical receptacle



optical demultiplexer

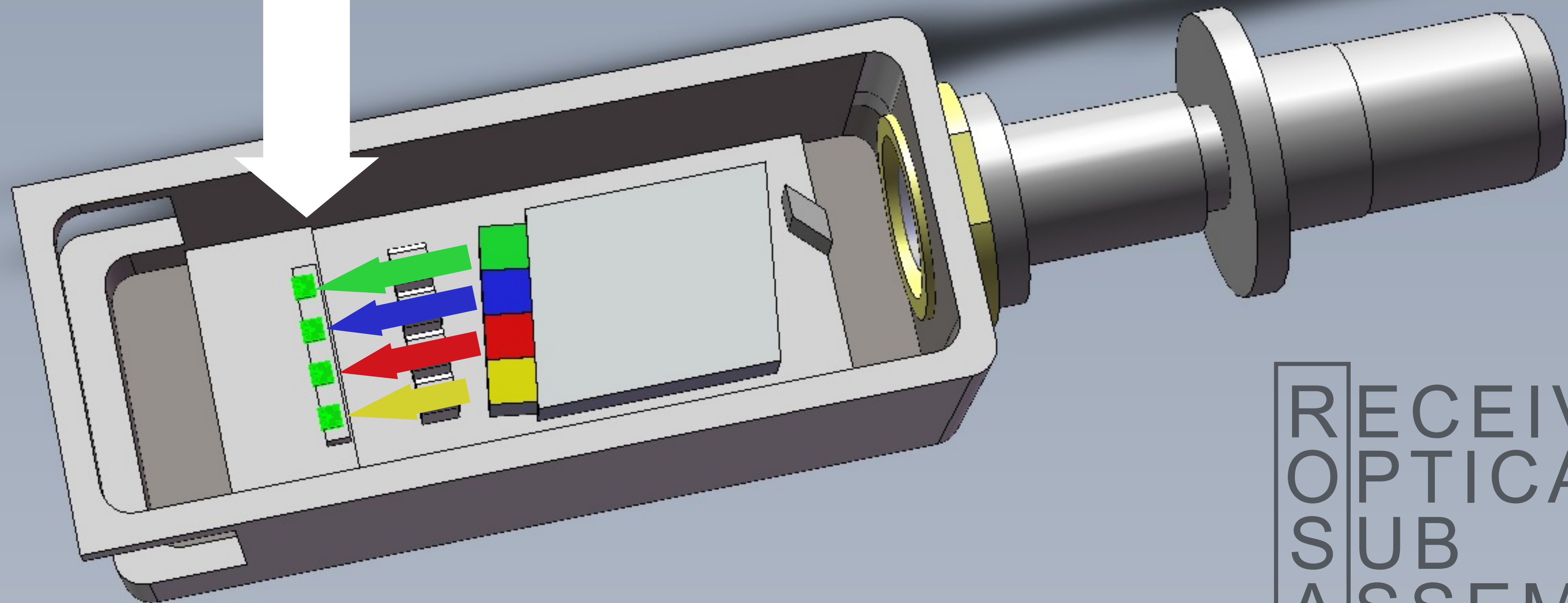


optical lenses



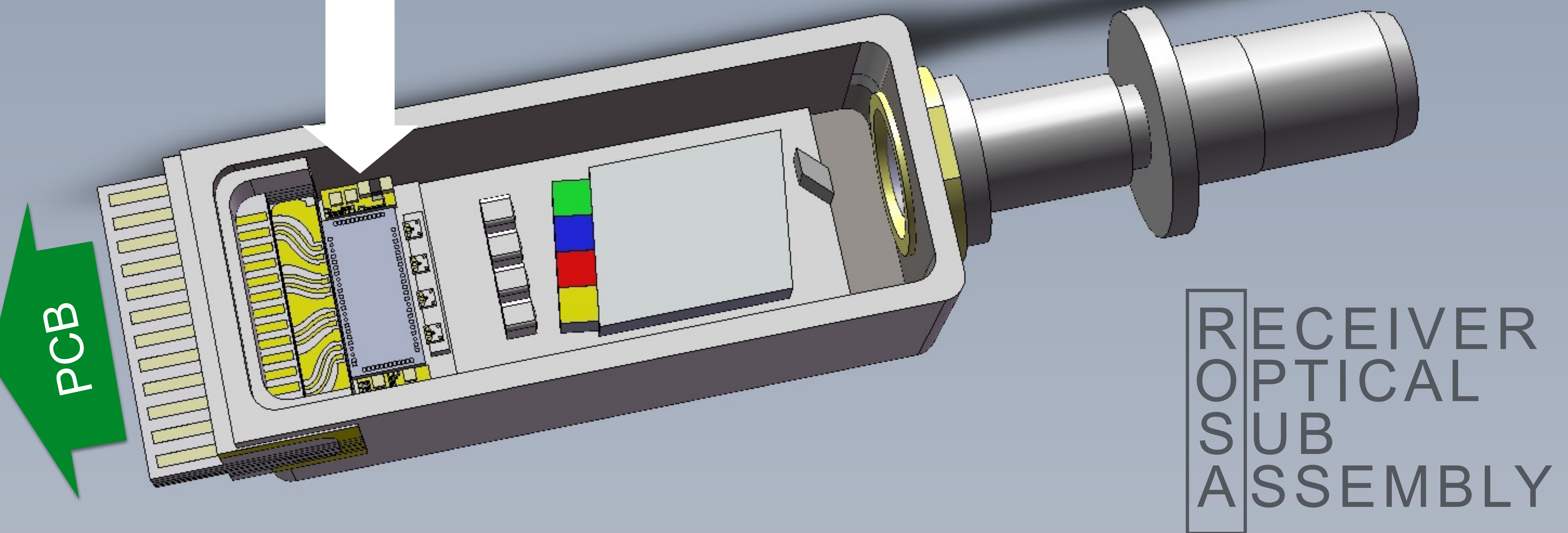
RECEIVER
OPTICAL
SUB
ASSEMBLY

PIN diode

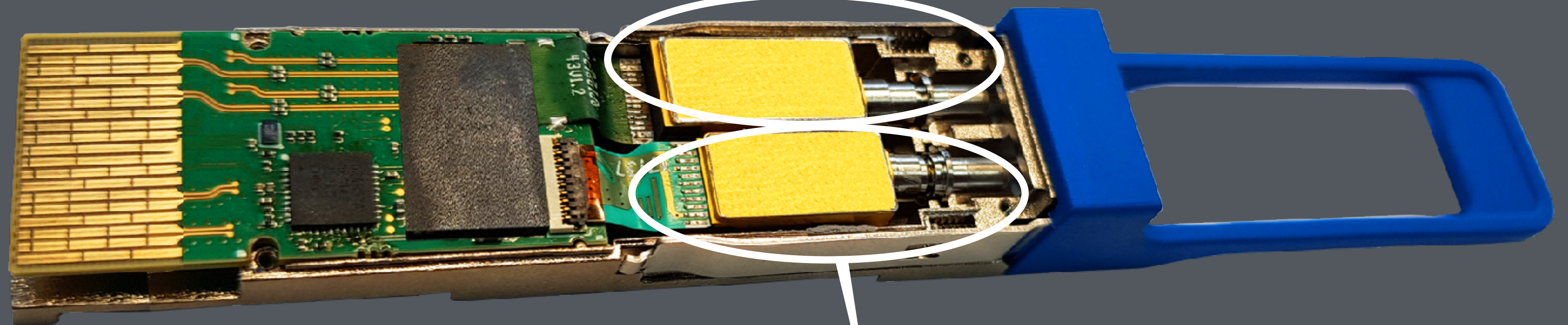


RECEIVER
OPTICAL
SUB
ASSEMBLY

amplifier

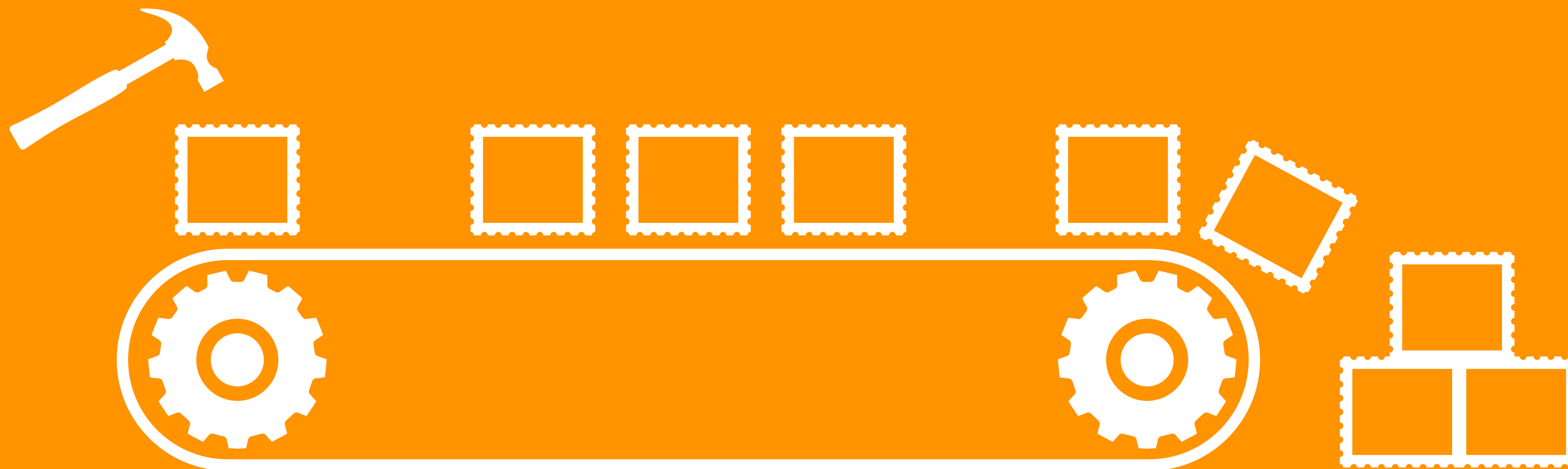


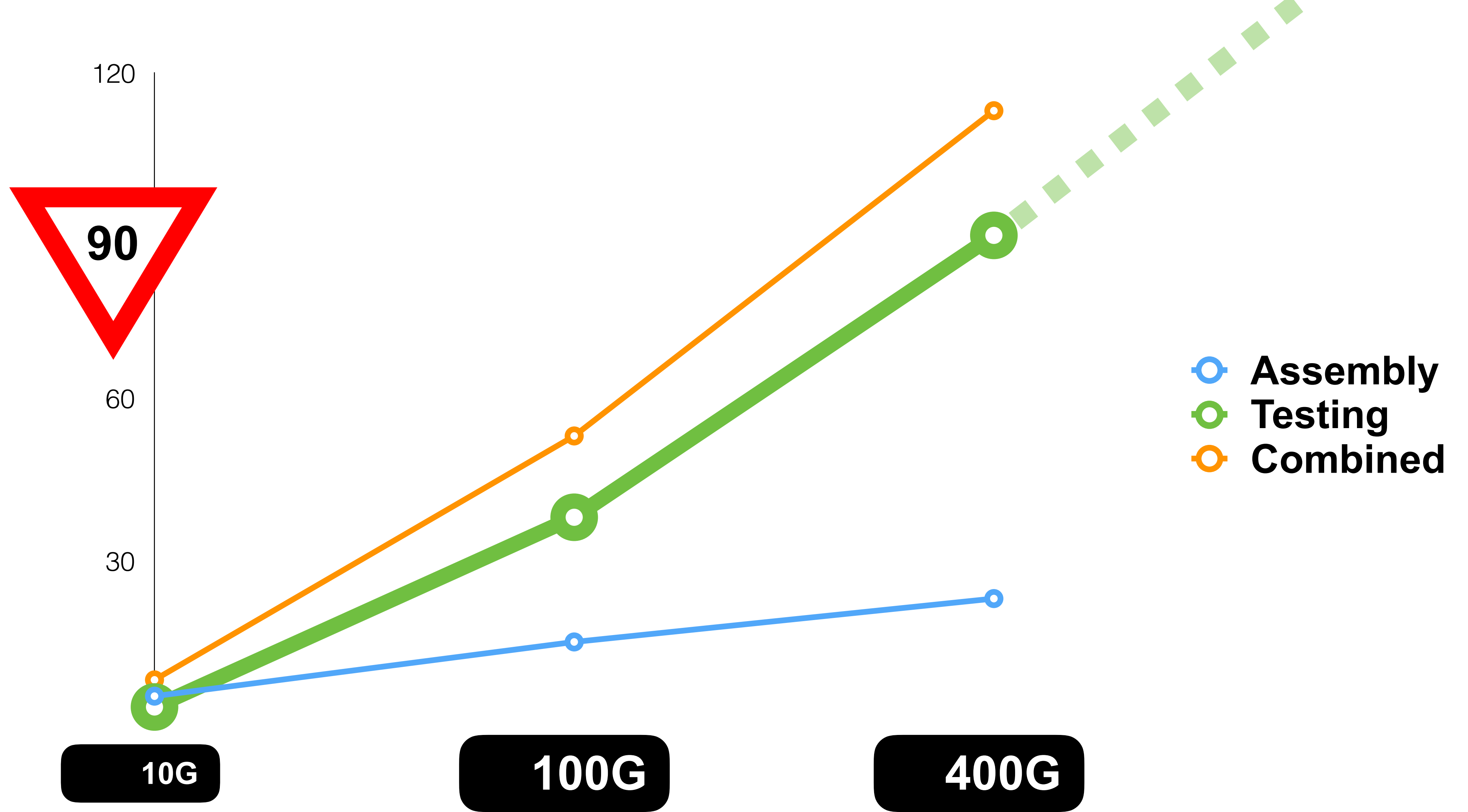
TRANSmitter



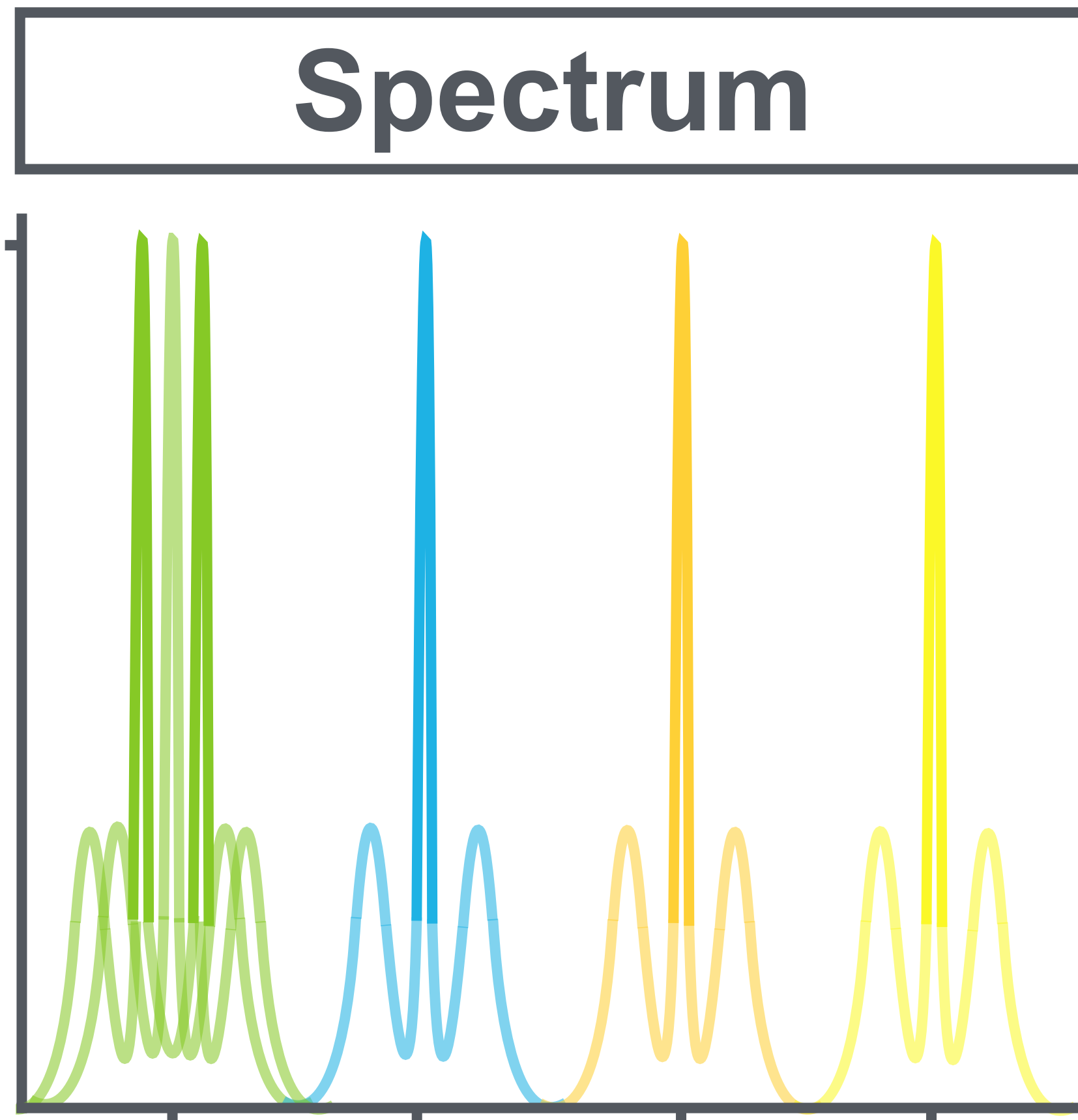
reCEIVER

production





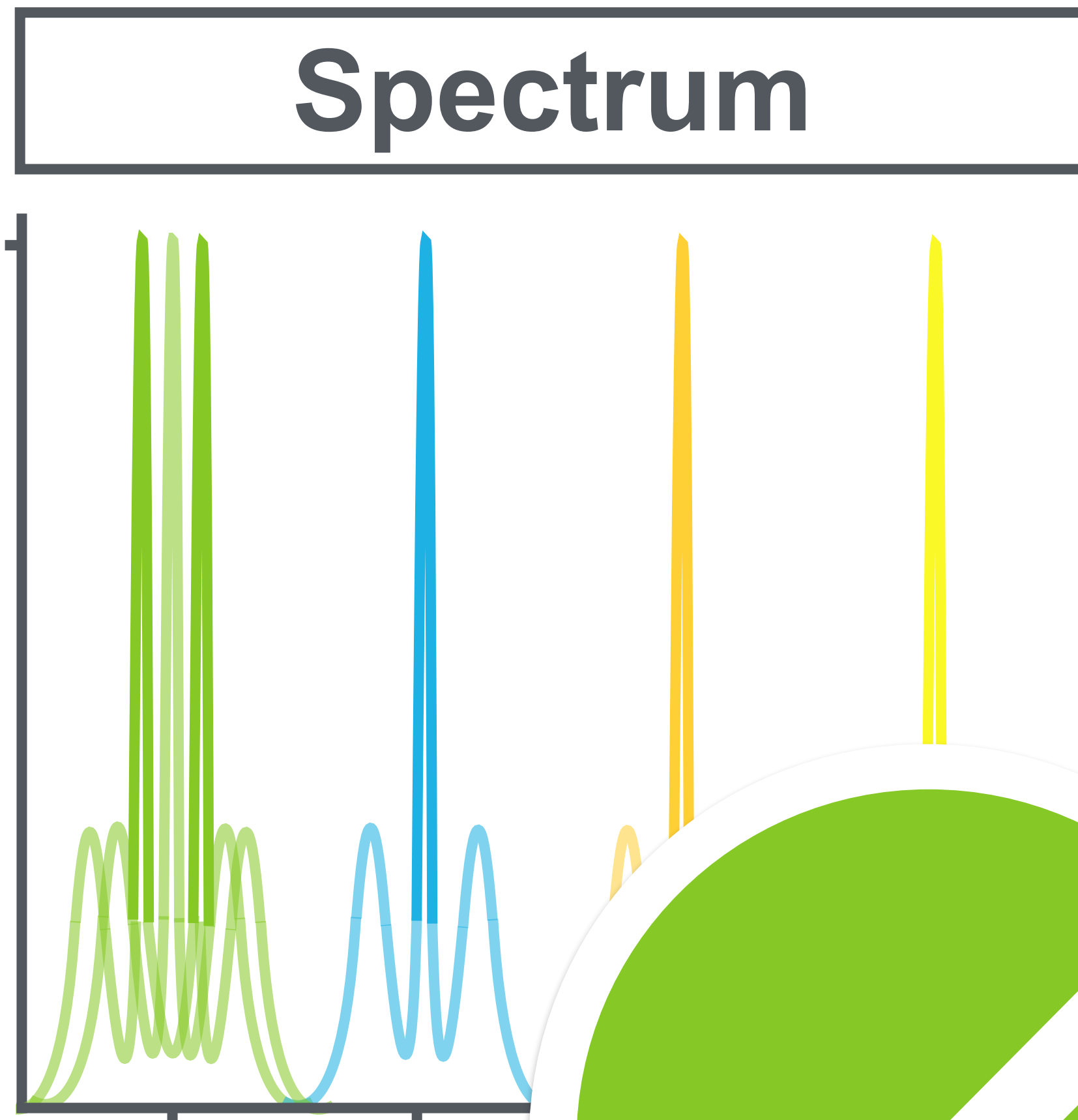
T I M I N G



0°C

+70°C

TEMPERATURE



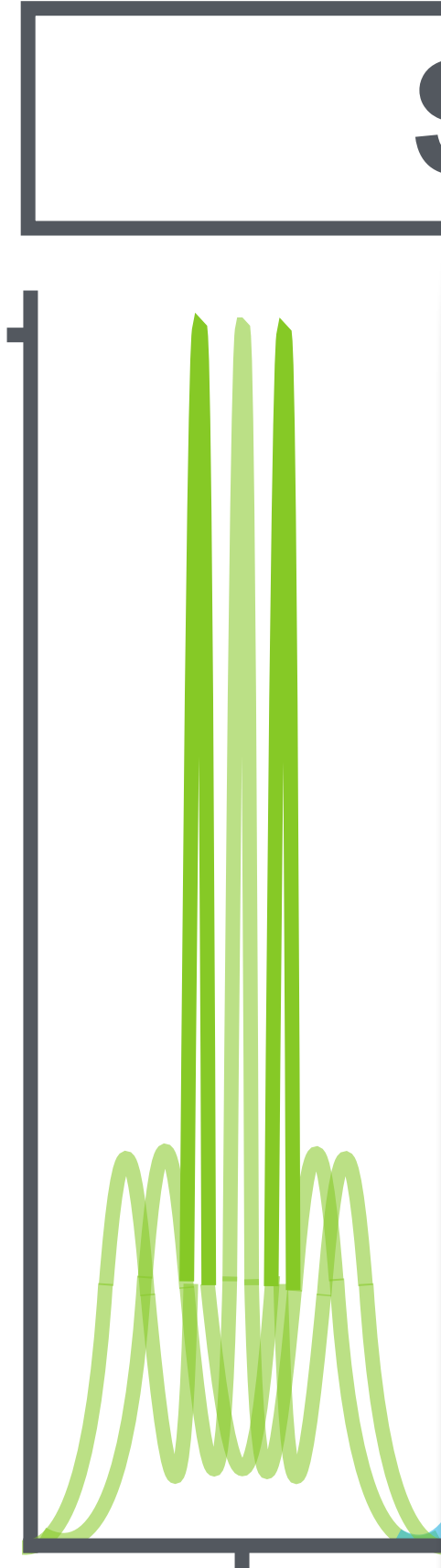
0°C

+70°C

T E M P E R A T U R E



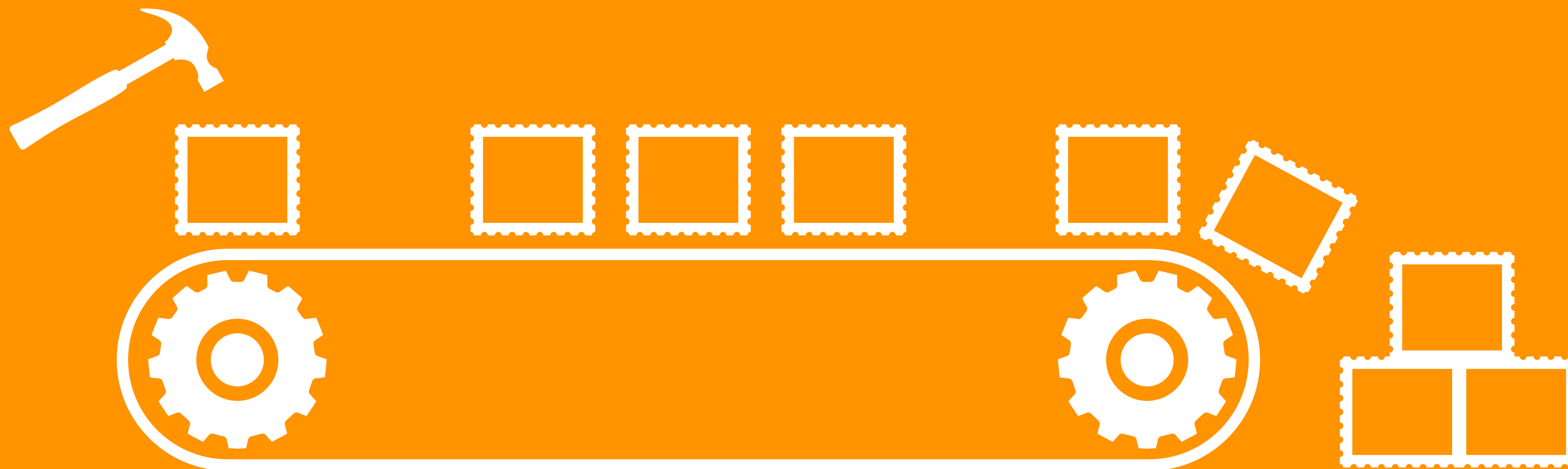
0°C



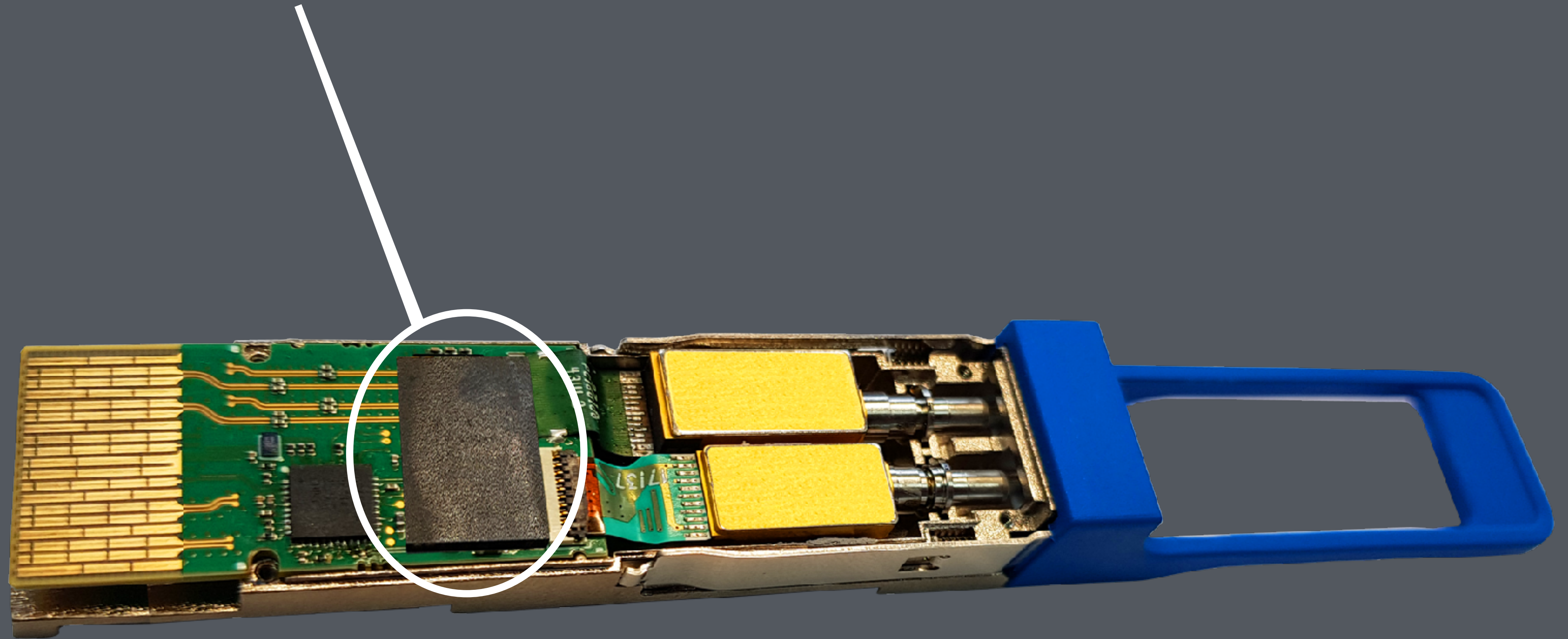
C

TEMPERATURE + TIME

production



DigitalSignalProcessor



Forward Error Correction



100G

FEC
ENC

FEC
DEC



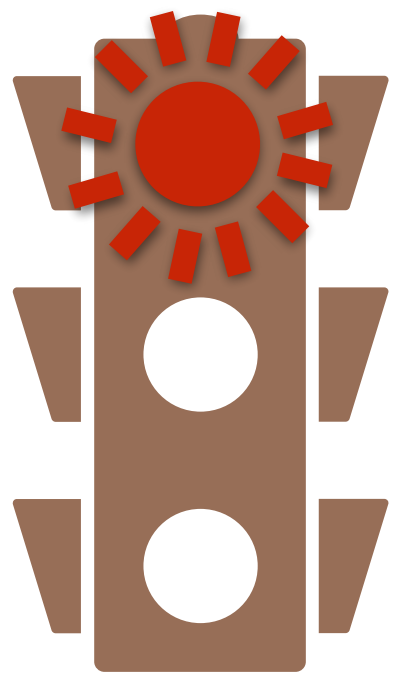
Switch

Switch

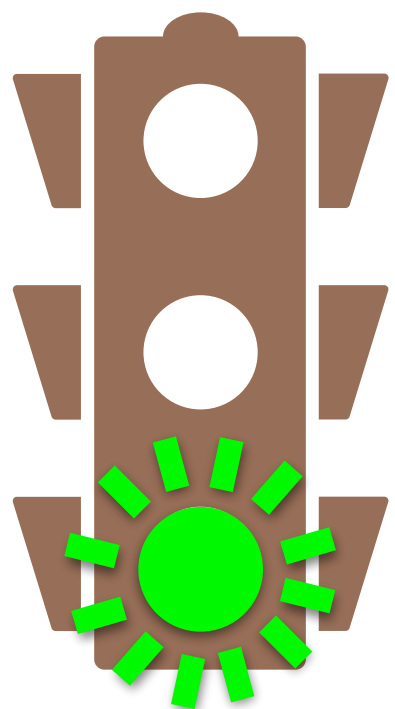
1



2



3



400G

FEC2
ENC

FEC2
DEC
+
FEC1
ENC

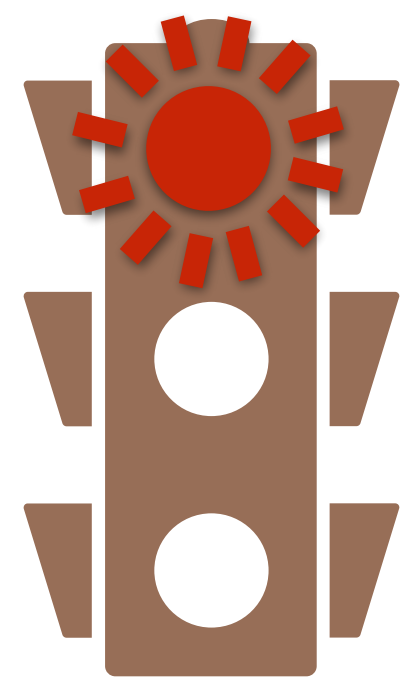


FEC1
DEC
+
FEC2
ENC

FEC2
DEC

Switch^{QSFP-DD}
OSFP

QSFP-DD^{OSFP} Switch

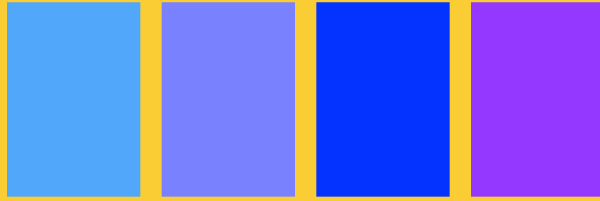
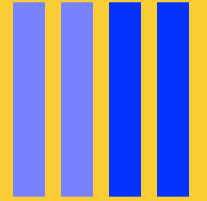


unequal # of
FEC's in line

new

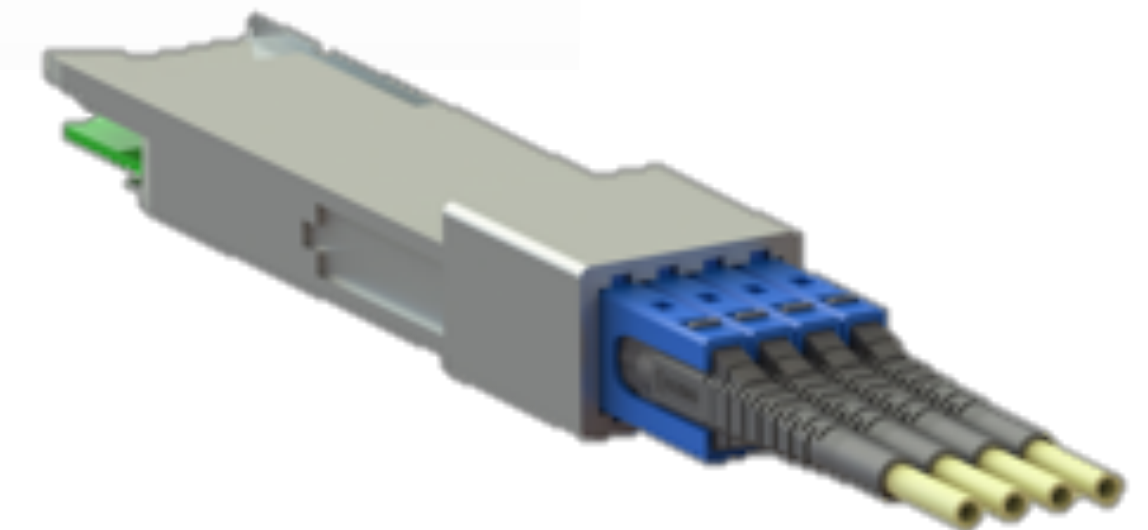


 FLEXOPTIX

400G LR4 as CWDM4  or LAN-WDM 

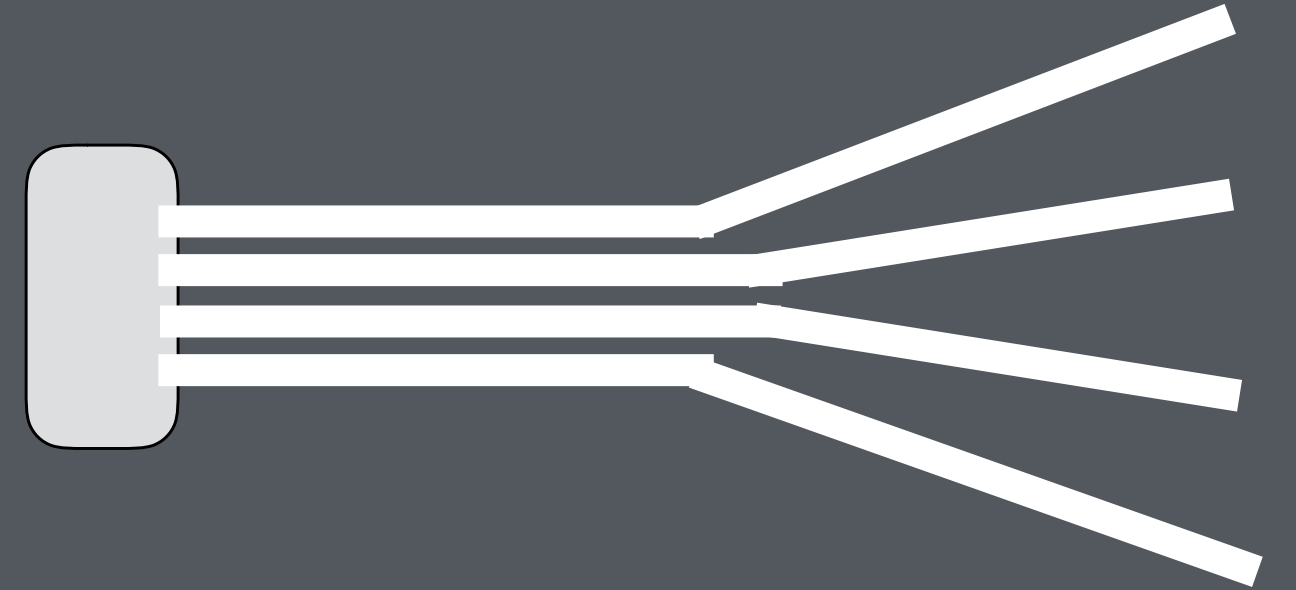
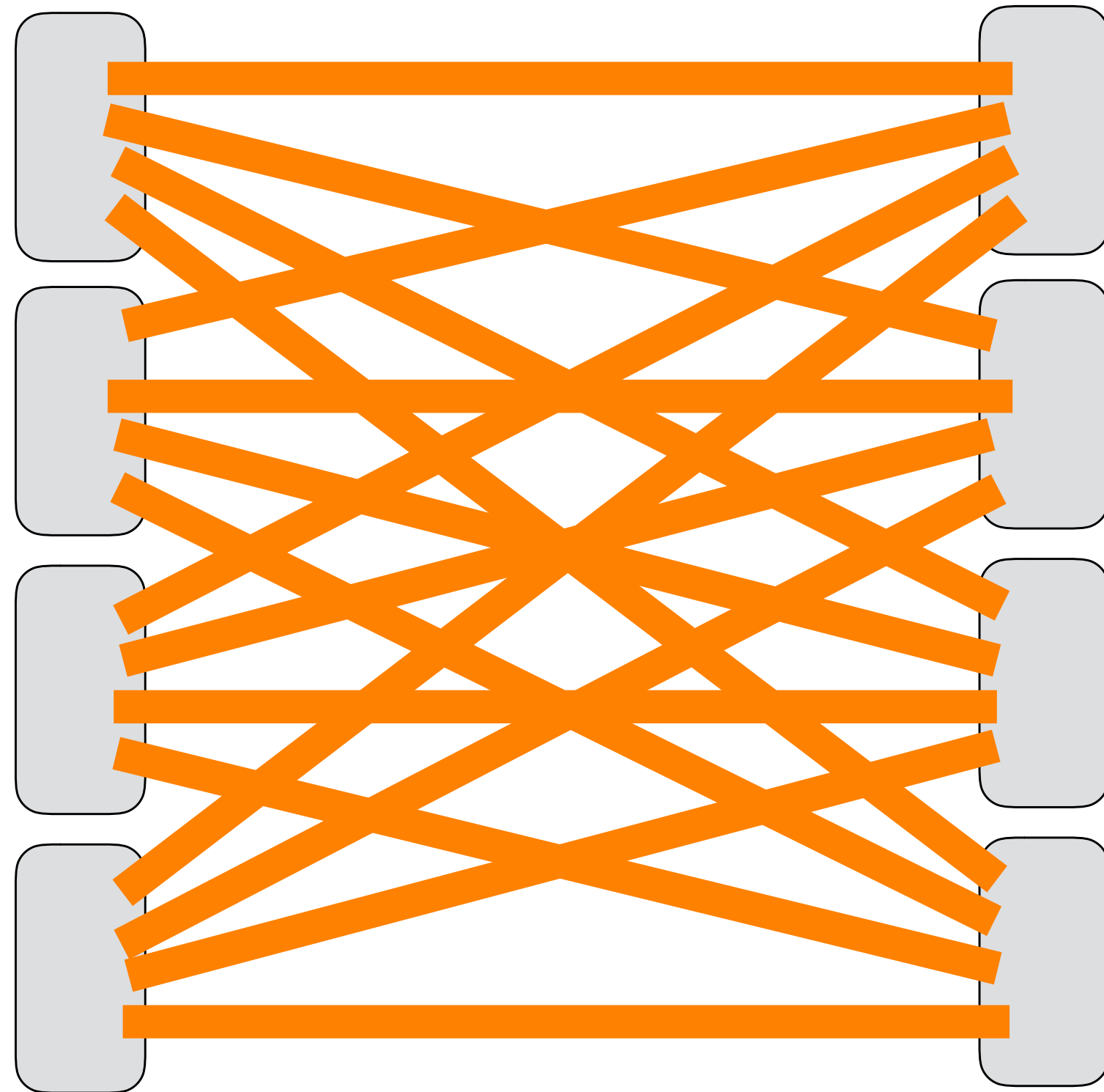
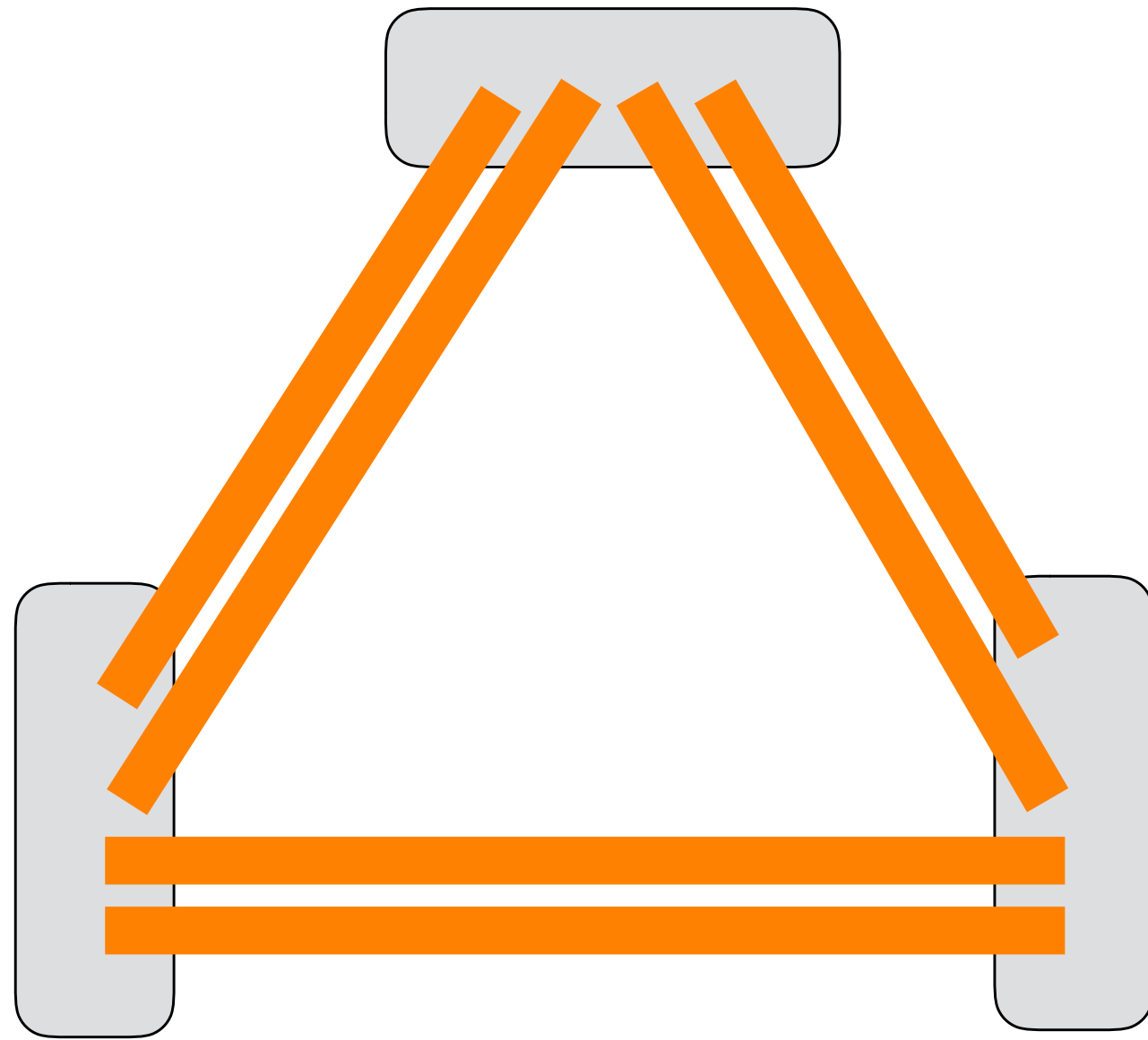
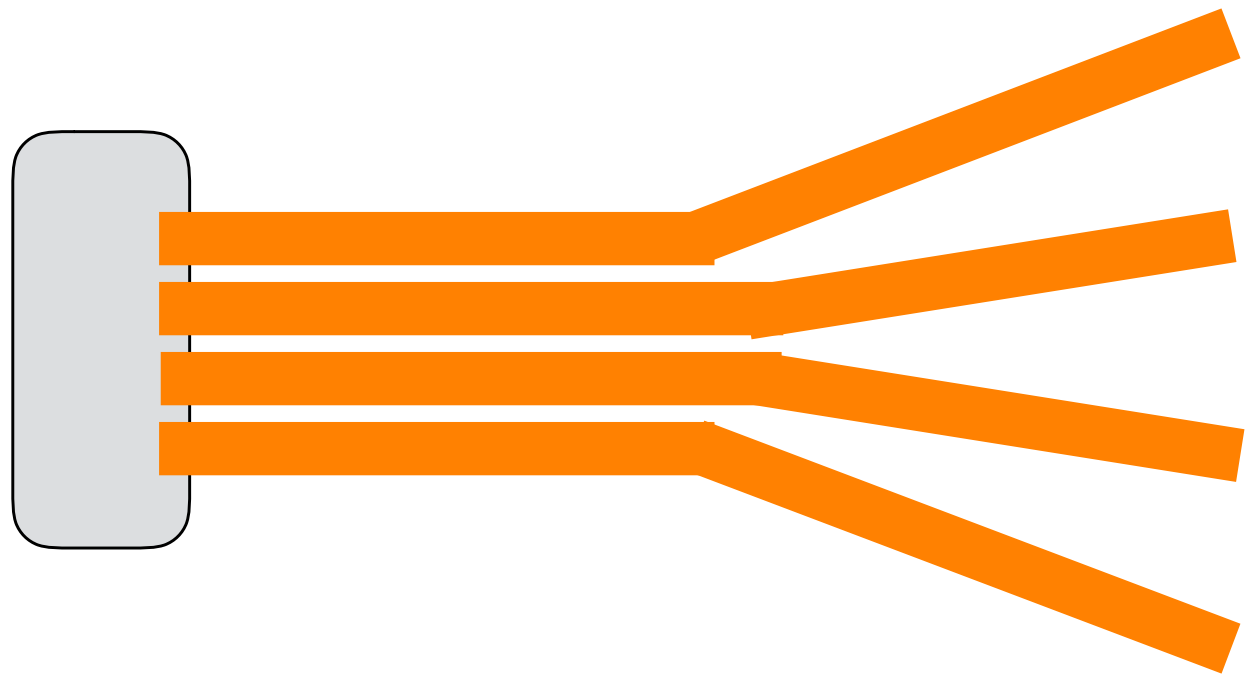
100G ER4 + ZR4

400G DR4
with SNTM plugs



source: SENKO Advanced Components,
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100G LR

40G DWDM LR

25G DWDM LR



THE FLEXBOX CREATORS

THOMAS.WEIBLE@FLEXOPTIX.NET



1
revolutionary
development



58
flexperts

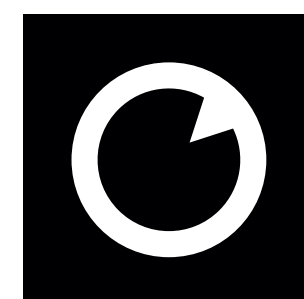


48615
transceiver
compatibilities





THANK YOU



FLEXOPTIX